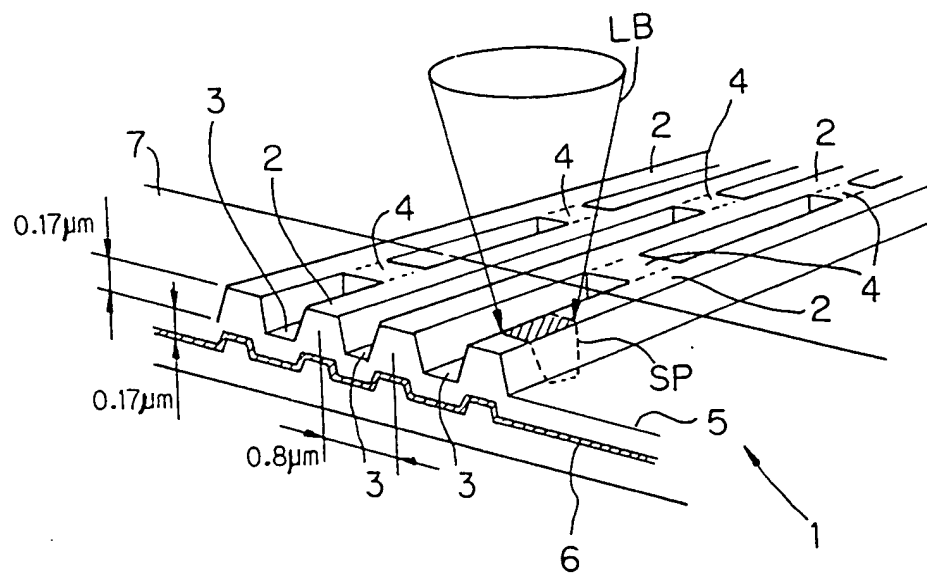
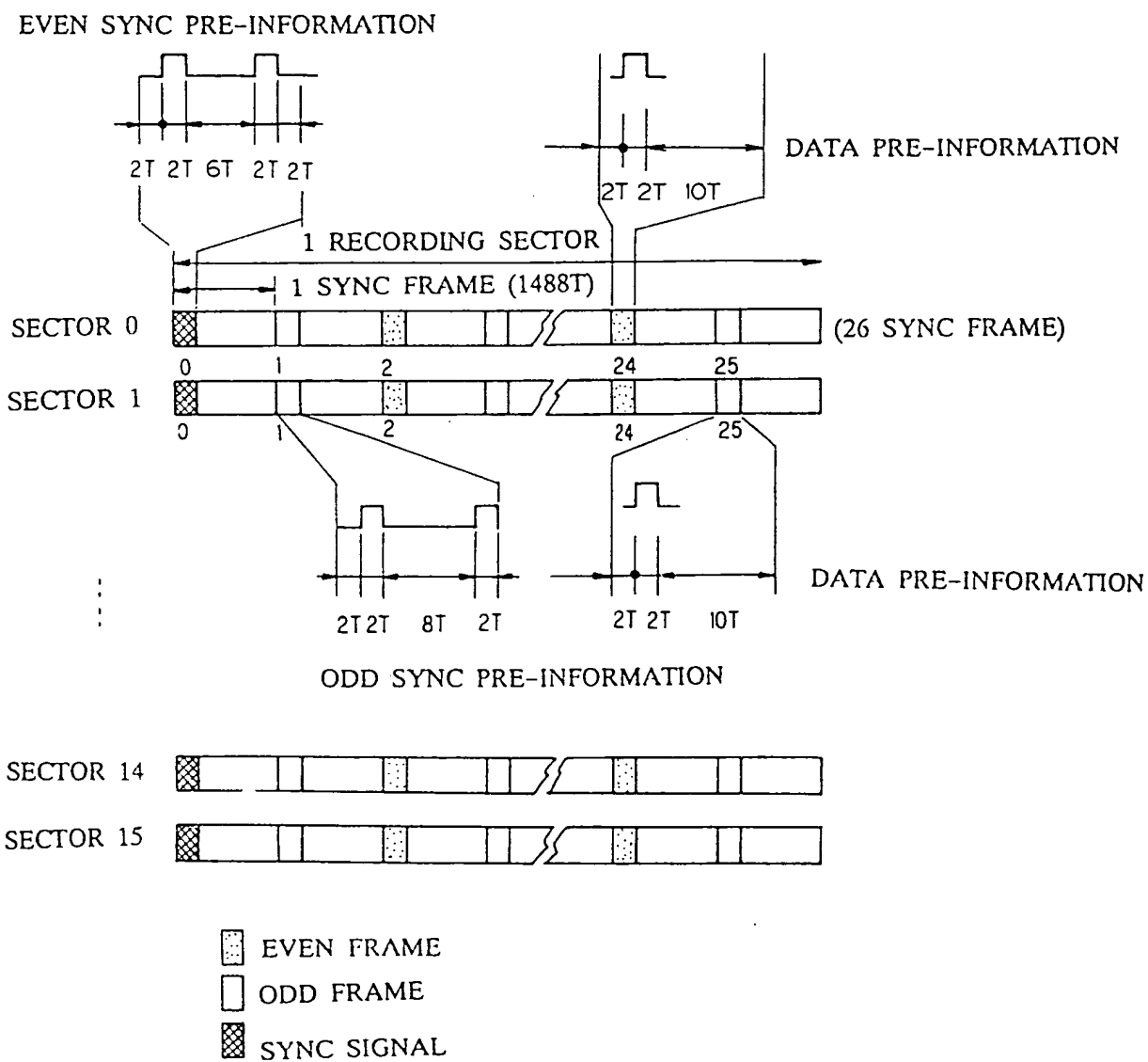


FIG. 1



# FIG. 2

## RECORDING FORMAT



# FIG. 3

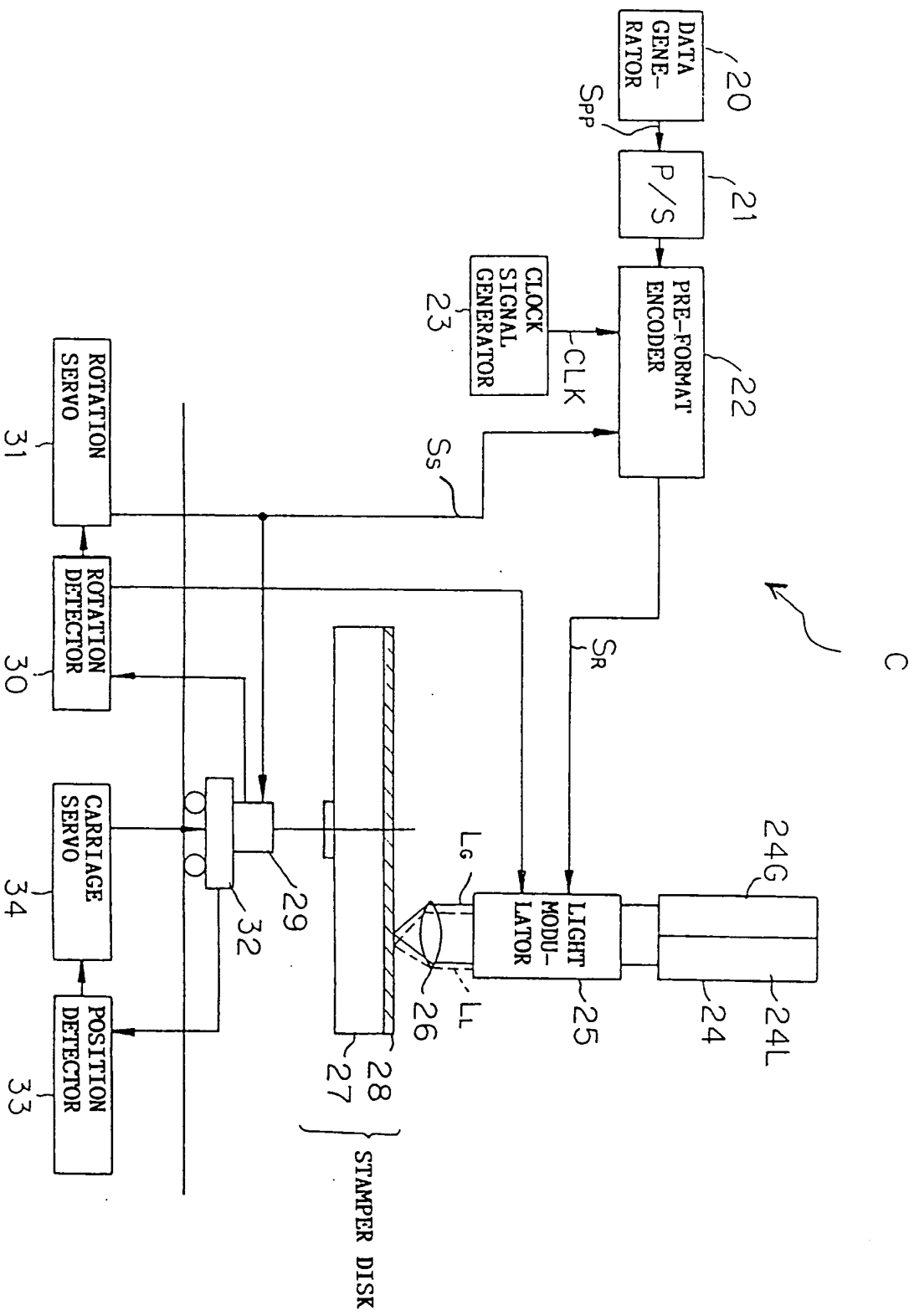


FIG. 4

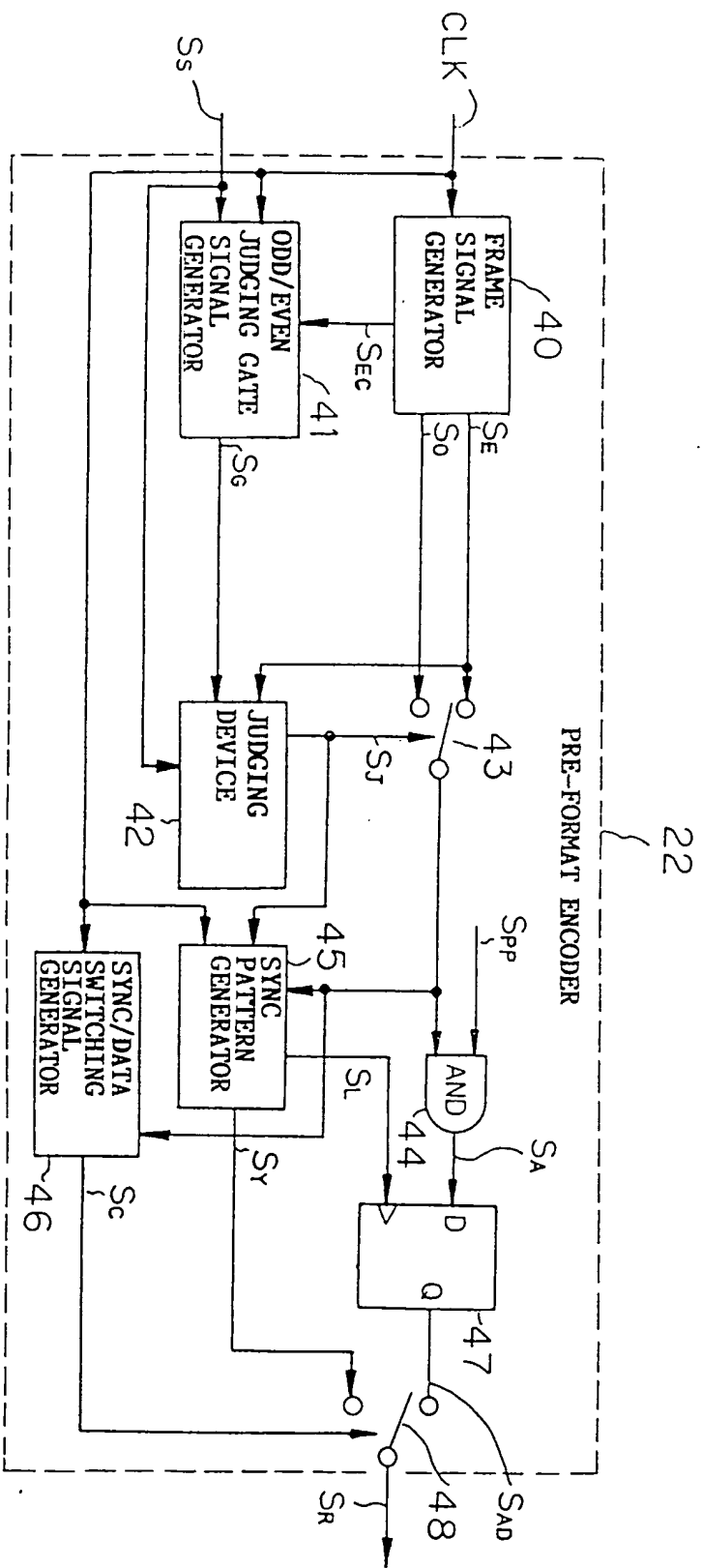
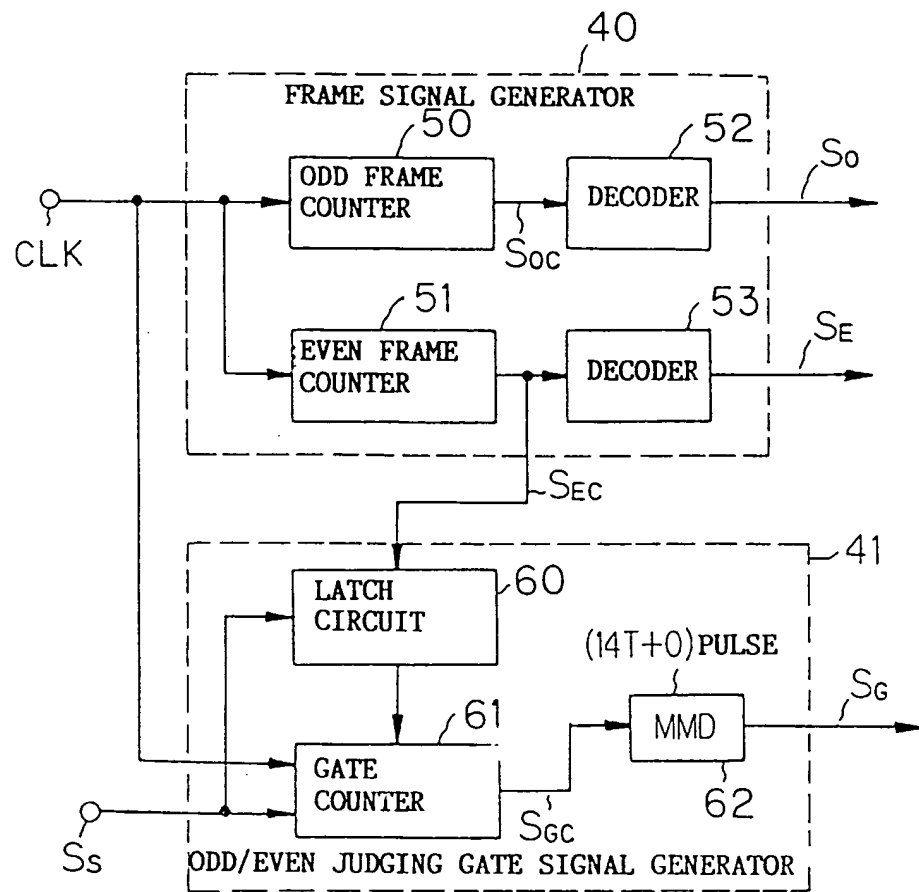
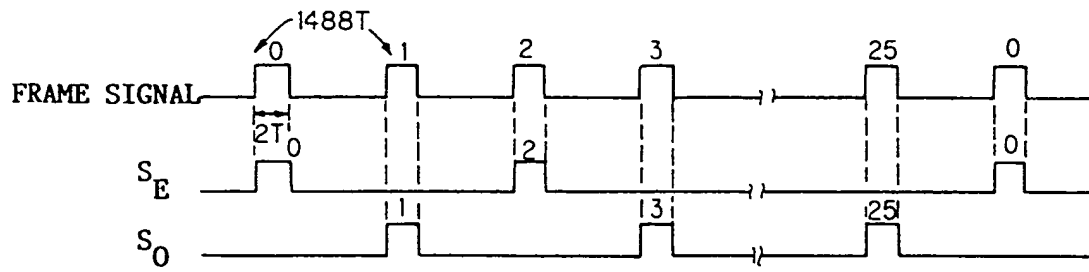


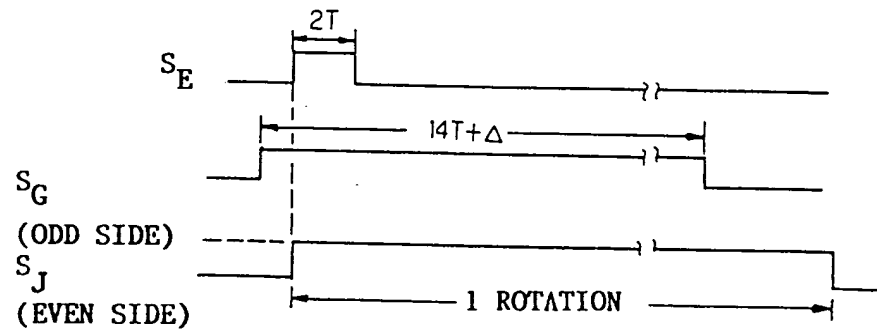
FIG. 5



# FIG. 6A



# FIG. 6B



# FIG. 6C

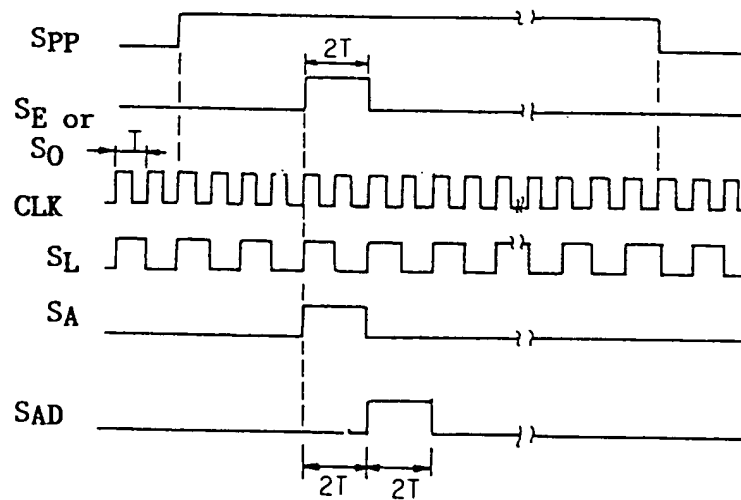
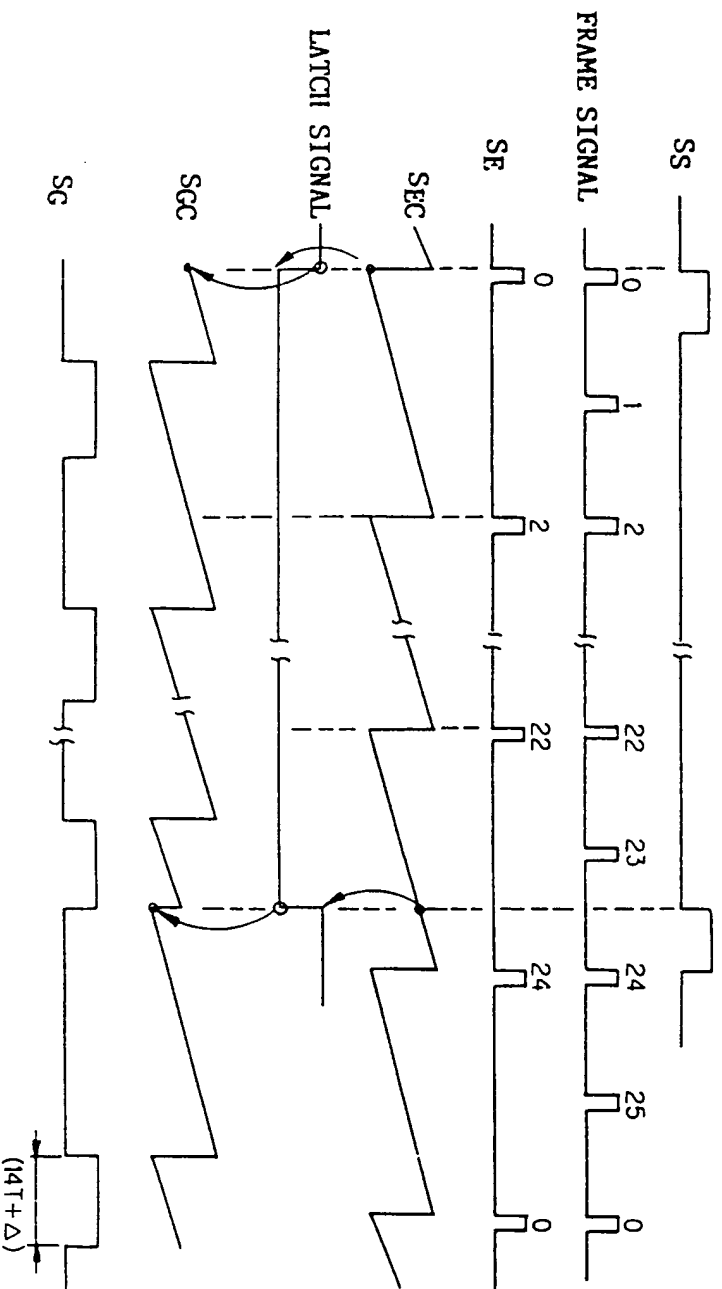
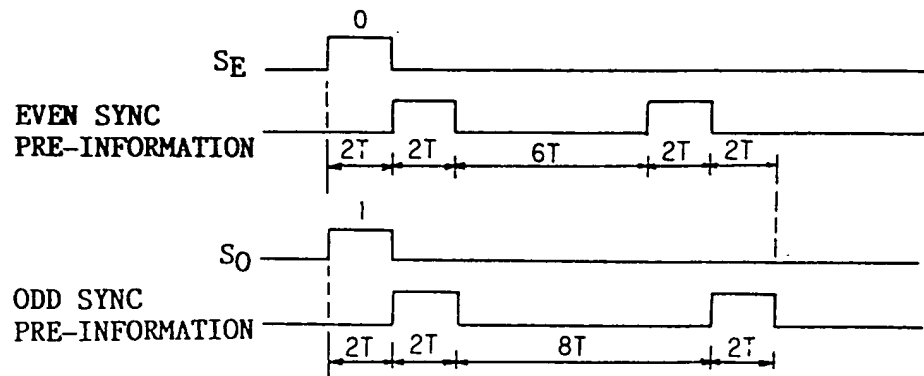


FIG. 7



# FIG. 8A



# FIG. 8B

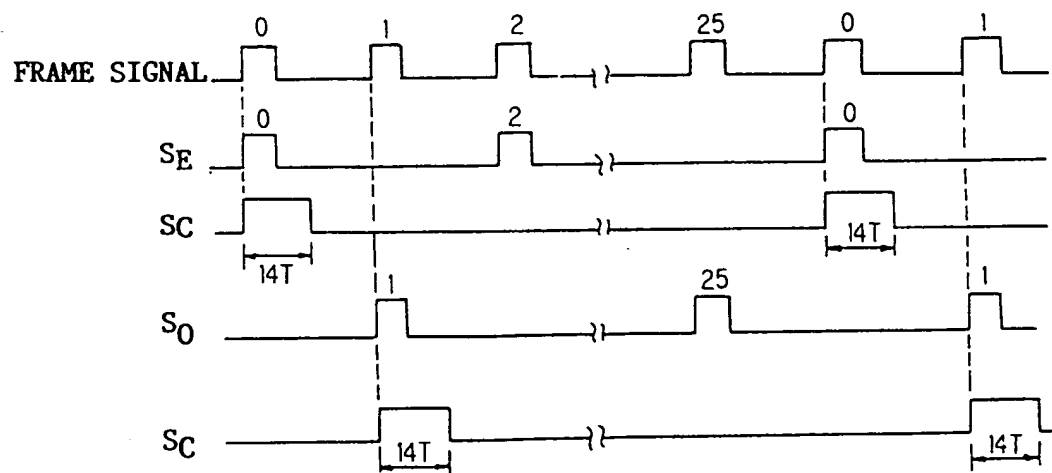




FIG. 9

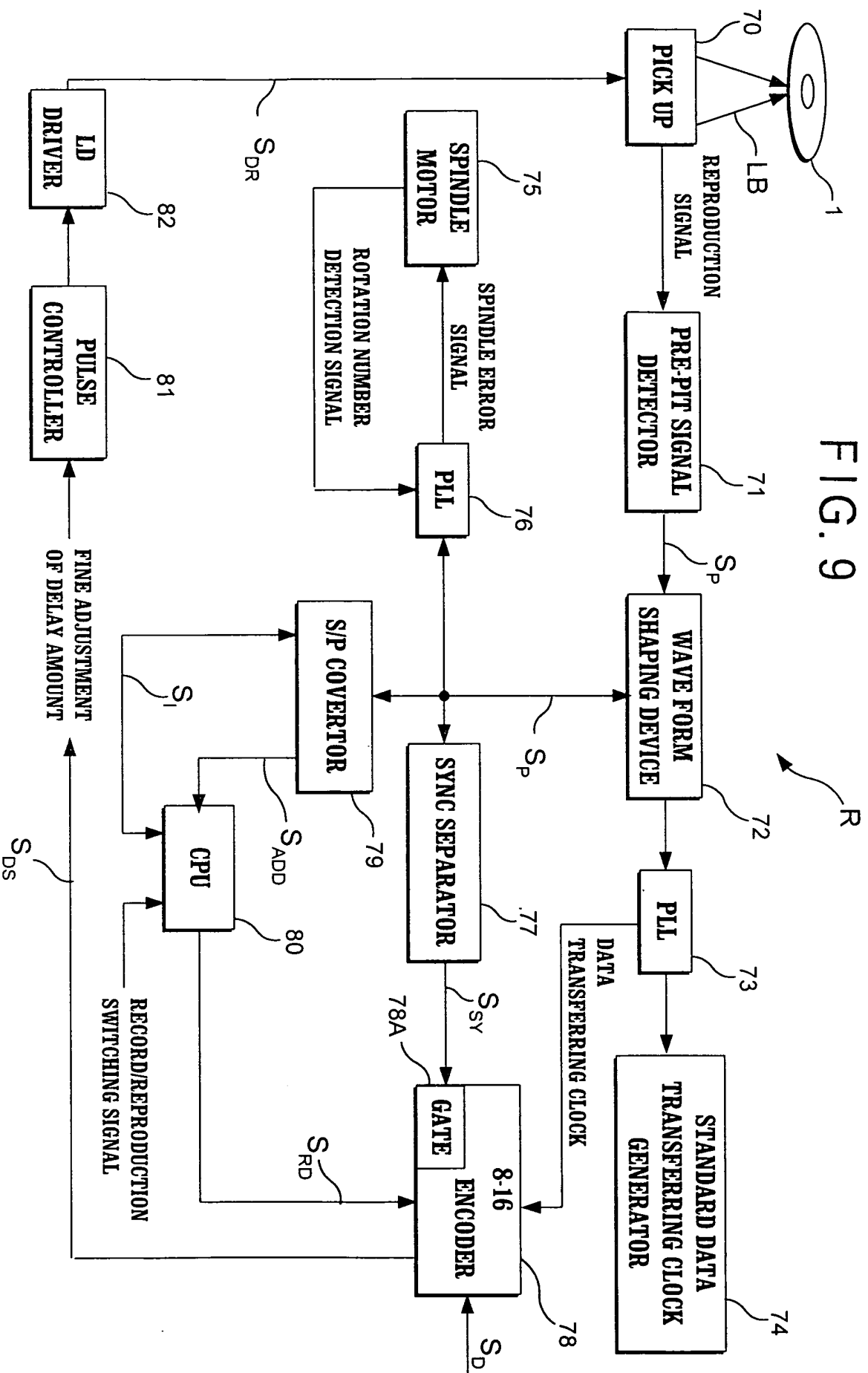
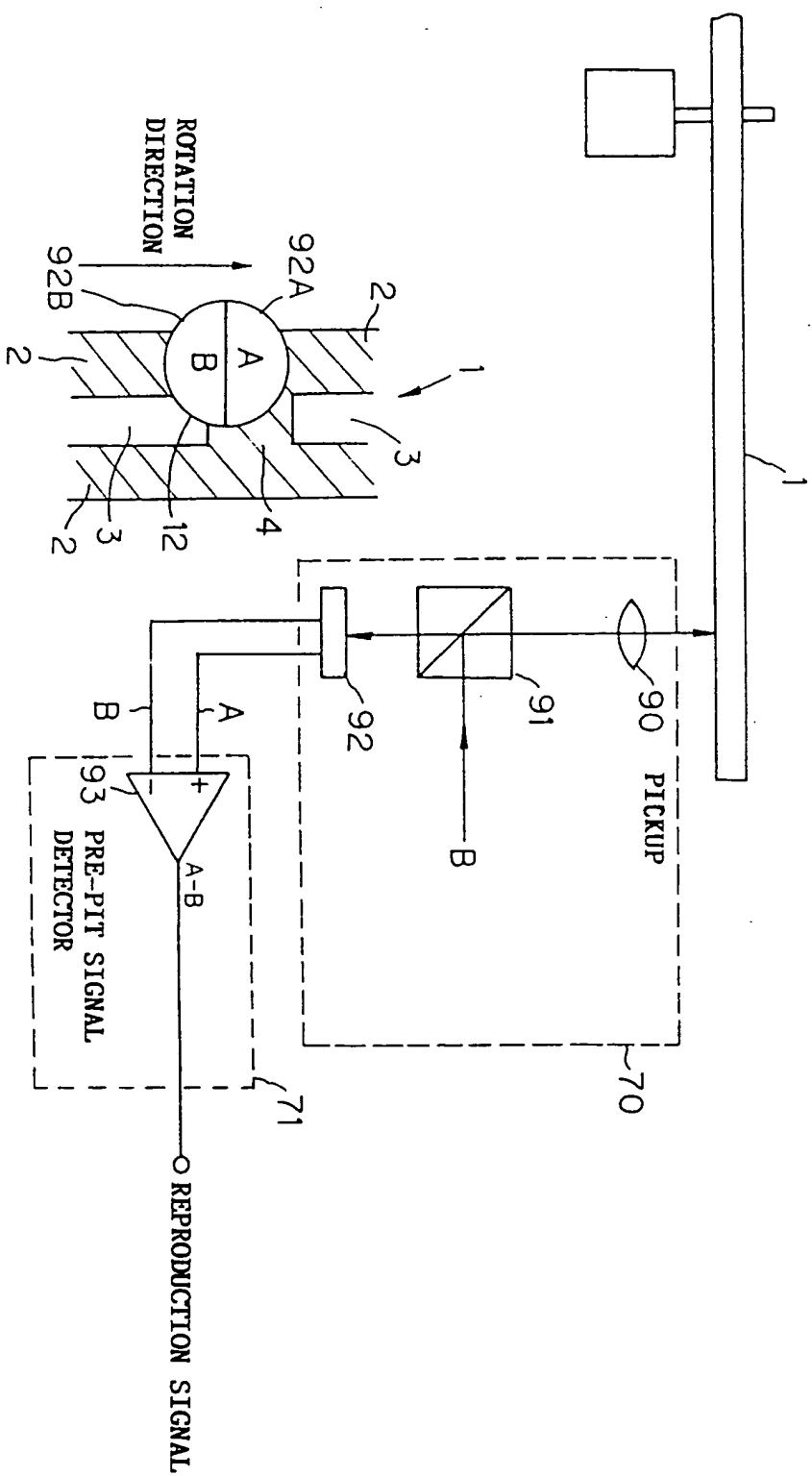
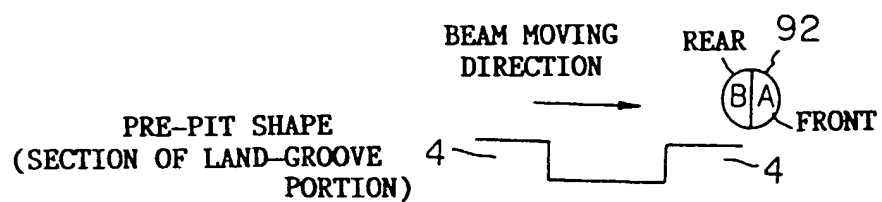


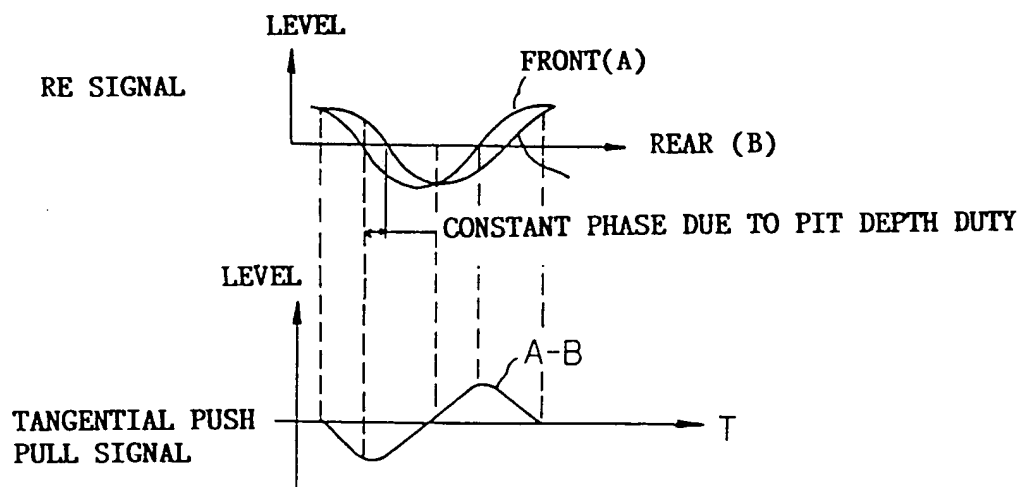
FIG. 10



# FIG.11A



# FIG.11B



# OPERATION AT CPU

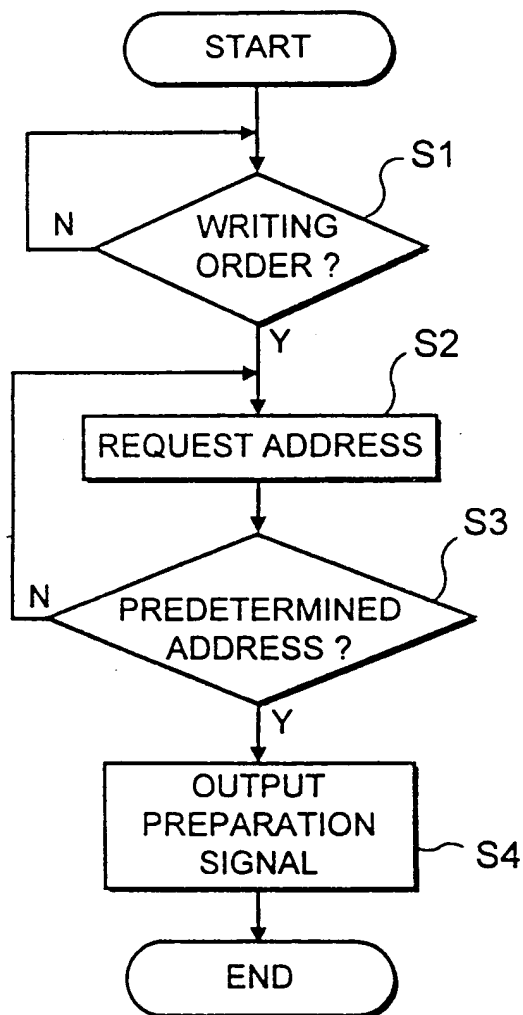


FIG. 12A

# OPERATION AT 8 - 16 ENCODER

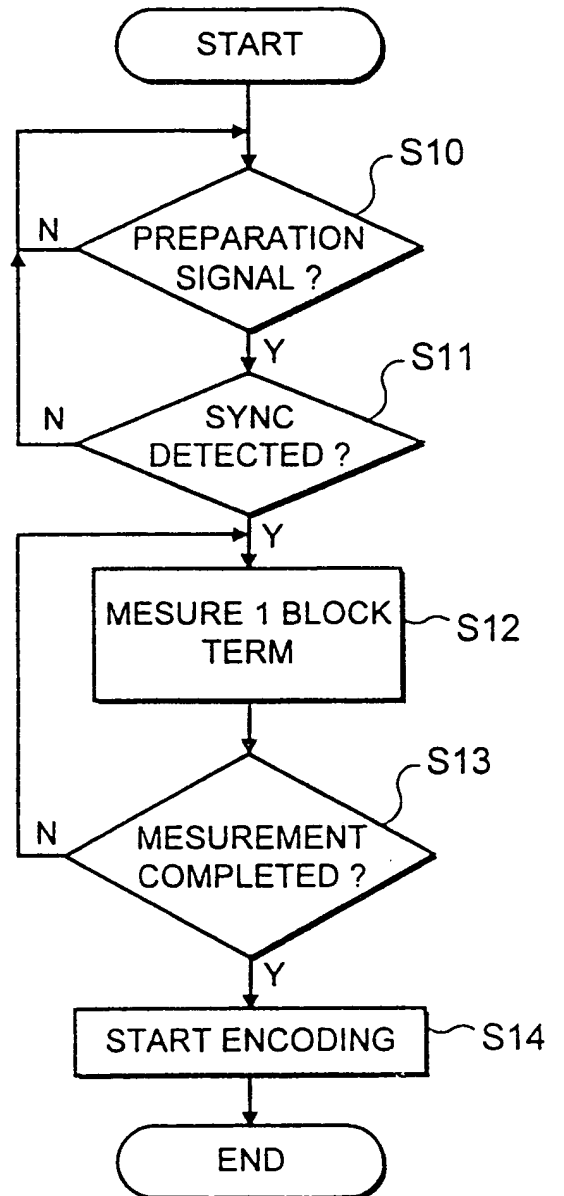


FIG. 12B

FIG.13

